

Fig. 1 (PRIOR ART)

App No.: Not Yet Assigned
Inventor: Chandra Mouli, et al.

Docket No.: M4065.0761/P761

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE
TRANSFER AND REDUCED LAG AND METHOD OF

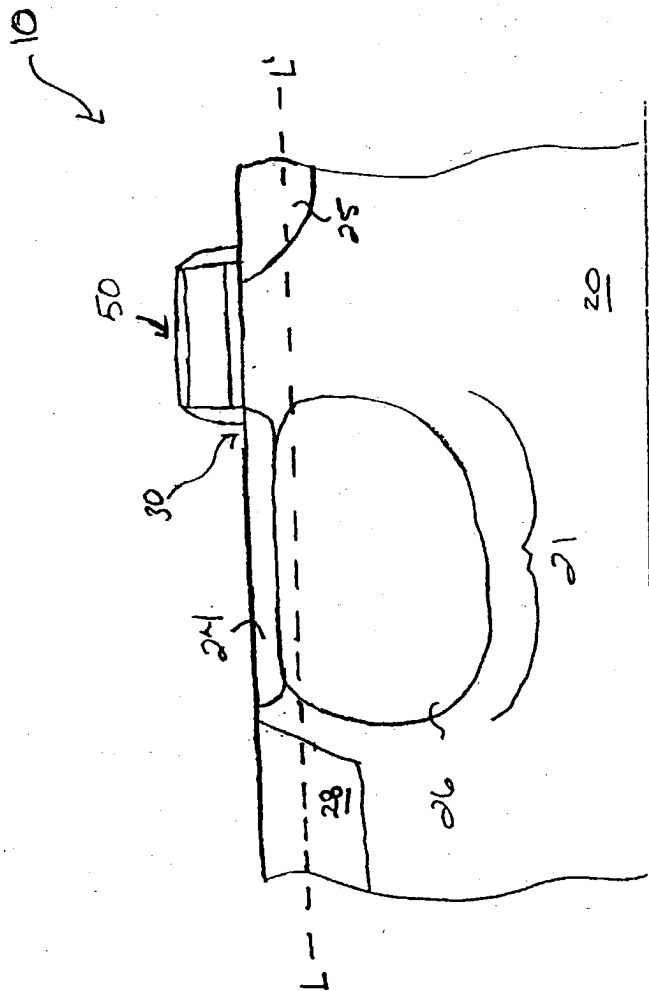


Fig. 2 (Prior Art)

App No.: Not Yet Assigned

Docket No.: M4065.0761/P761

Inventor: Chandra Mouli, et al.

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE
TRANSFER AND REDUCED LAG AND METHOD OF

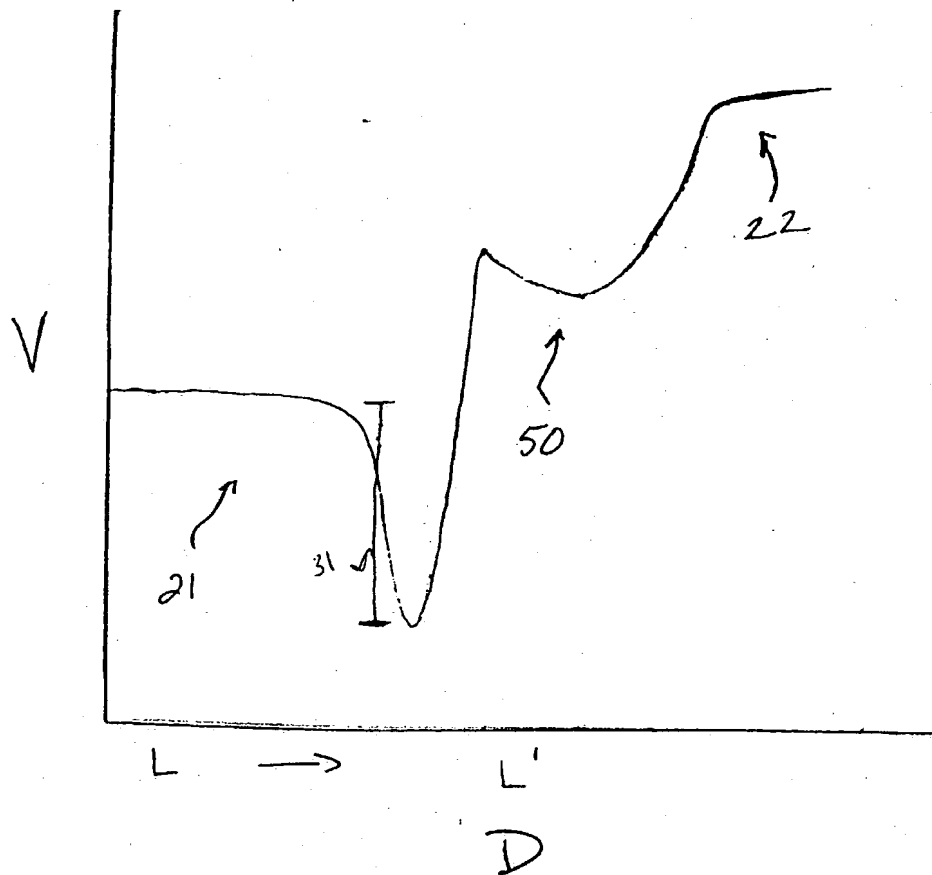


Fig. 3

App No.: Not Yet Assigned
Inventor: Chandra Mouli, et al.

Docket No.: M4065.0761/P761

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE
TRANSFER AND REDUCED LAG AND METHOD OF

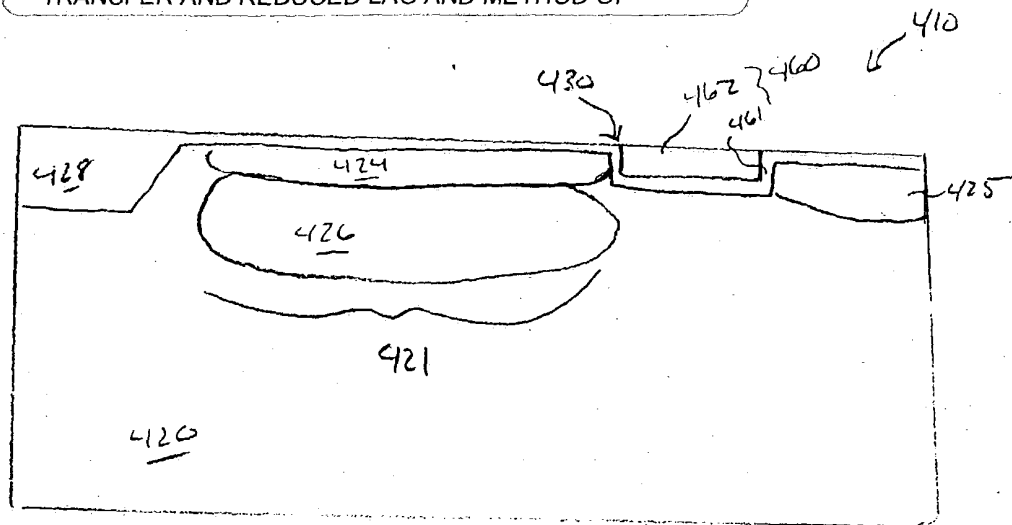


FIG. 4

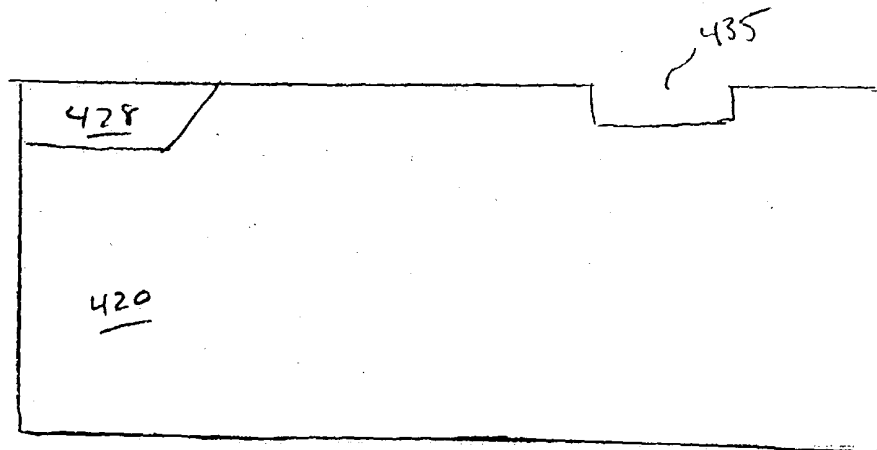


FIG. 5A

App No.: Not Yet Assigned Docket No.: M4065.0761/P761
Inventor: Chandra Mouli, et al.
Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE
TRANSFER AND REDUCED LAG AND METHOD OF

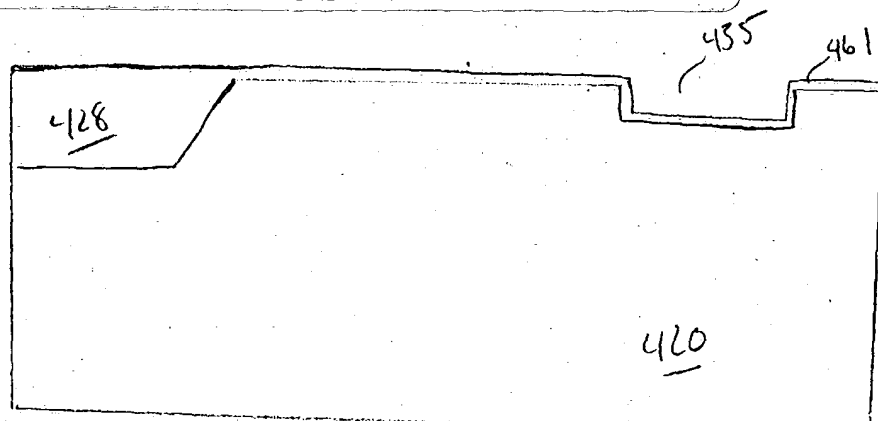


FIG. 5B

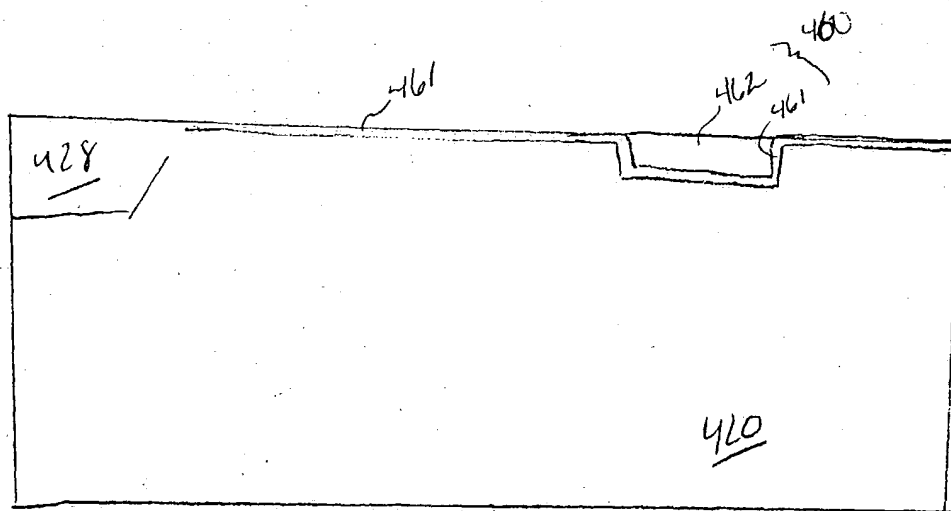


FIG. 5C

App No.: Not Yet Assigned
Inventor: Chandra Mouli, et al.

Docket No.: M4065.0761/P761

Title: REDUCED BARRIER PHOTODIODE/TRANSFER GATE
DEVICE STRUCTURE FOR HIGH EFFICIENCY CHARGE
TRANSFER AND REDUCED LAG AND METHOD OF

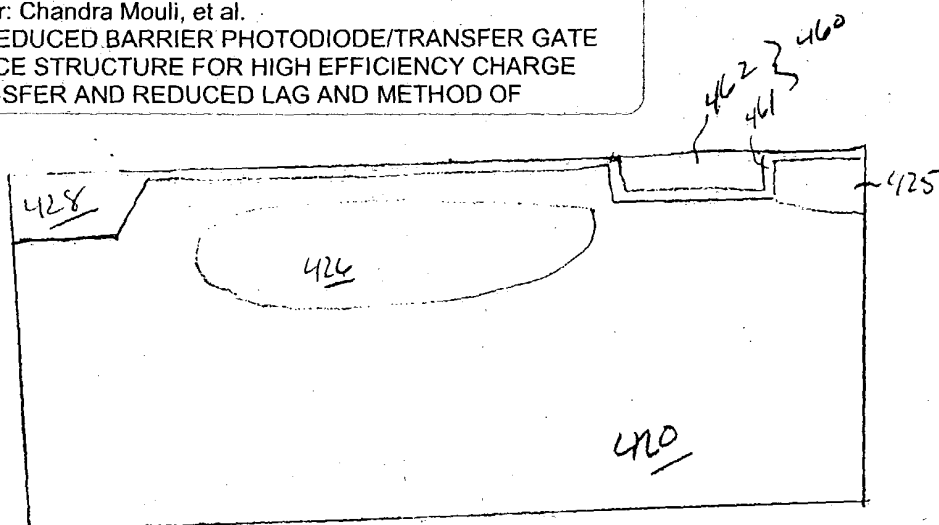


FIG. 5D

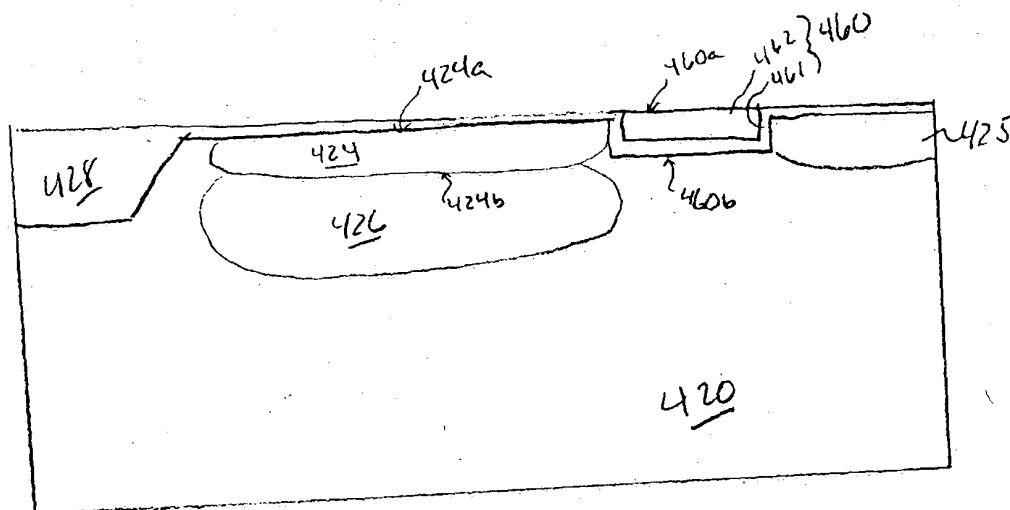


FIG. 5E

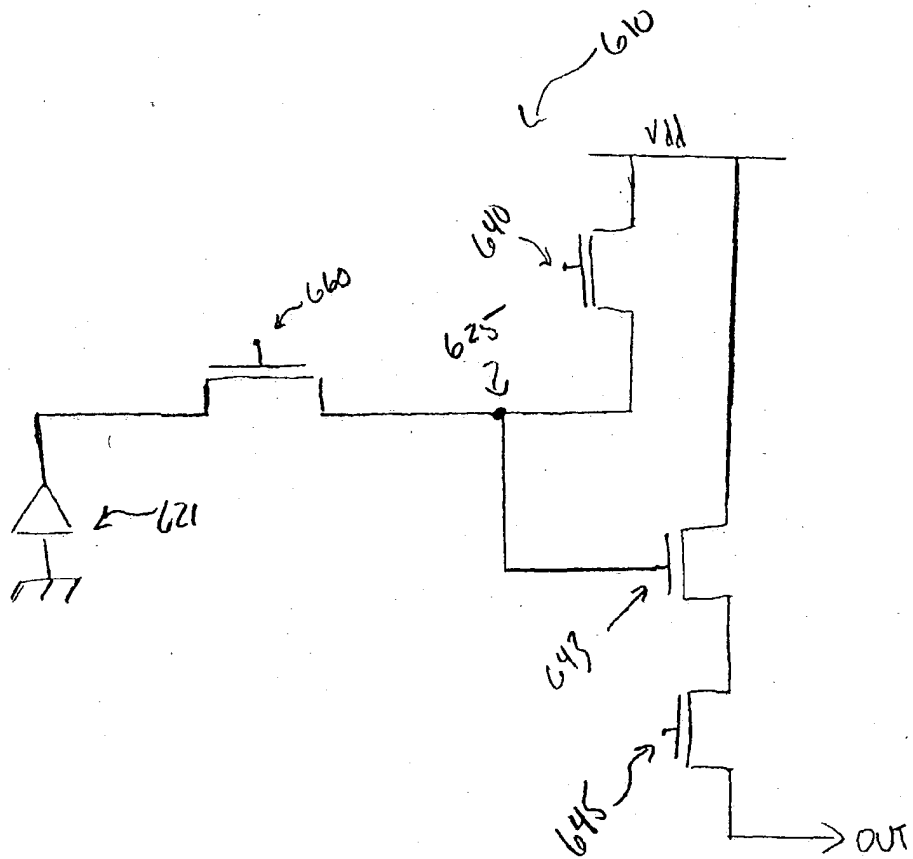


FIG. 6

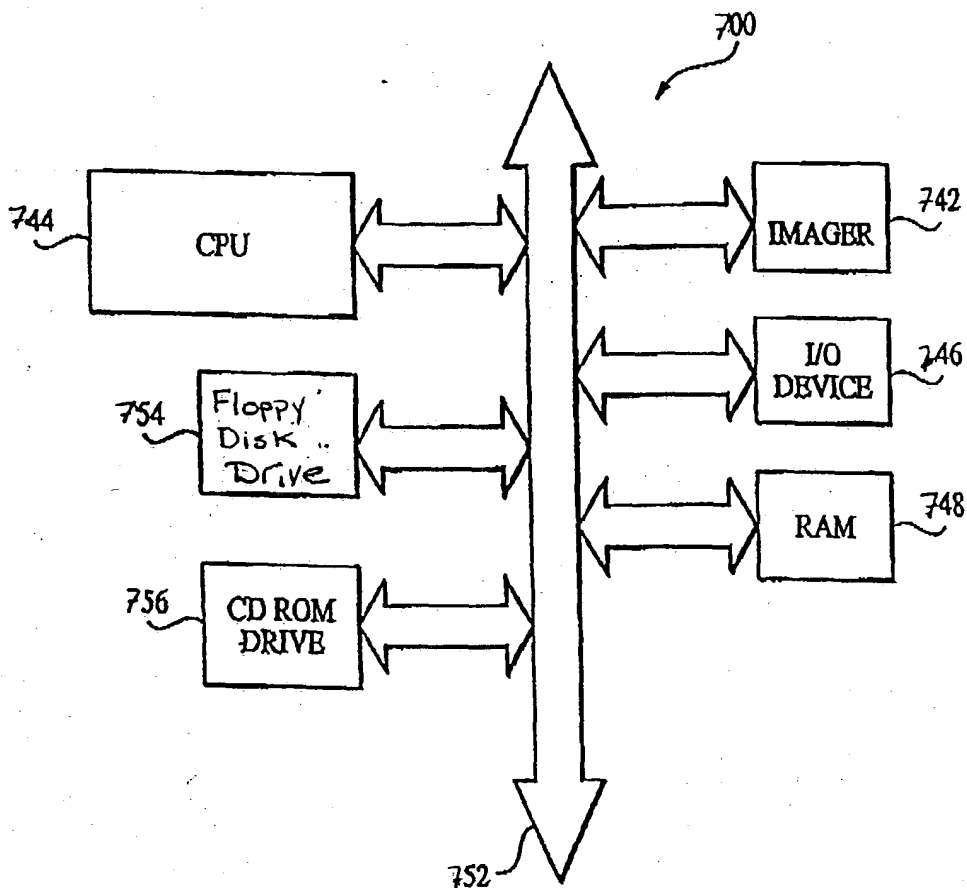


Fig. 7